

T814 5200-30-42

Key Parameters

V_{DRM}	3000~4200	V
$I_{T(AV)}$	5250	A
I_{TSM}	83.5	kA
V_{TO}	0.98	V
r_T	0.1	mΩ

Applications

- SVC
- Motor drive
- Industry converter

Features

- Double-side cooling
- High power capability
- Low loss

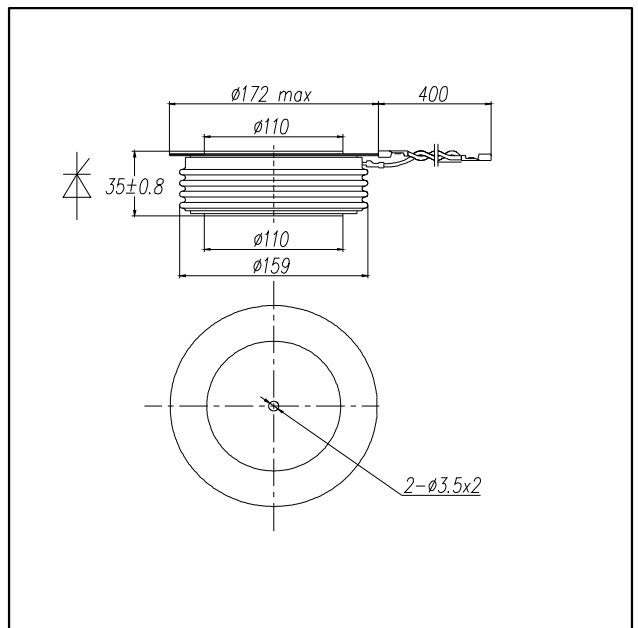
Thermal & Mechanical Data

Symb.	Parameter	Min	Type	Max	Unit
R_{thjc}	Thermal Resistance Junction to Case	-	-	0.004	K / W
R_{thcs}	Thermal Resistance Case to Heatsink	-	-	0.0008	K / W
T_{vj}	Junction Temperature	-40	-	125	°C
T_{stg}	Storage Temperature	-40	-	140	°C
F	Mounting Force	-	120	-	kN
m	Weight	-	3.5	-	kg
HCRP	Creep distance	34	-	-	mm

Voltage Ratings

Device Type	$V_{DRM}/V_{RRM}(V)$	Test Conditions
T814 5200-30	3000	$T_{vj} = 125\text{ °C}$
T814 5200-32	3200	$I_{DRM} = 700\text{ mA}$
T814 5200-34	3400	$I_{RRM} = 700\text{ mA}$
T814 5200-36	3600	
T814 5200-38	3800	$V_{DM} = V_{DRM}, V_{RM} = V_{RRM}$
T814 5200-40	4000	$t_p = 10\text{ ms}$
T814 5200-42	4200	
		$V_{DSM} = V_{DRM}$ $V_{RSM} = V_{RRM} + 100$

Outline



Current Ratings

Symb.	Parameter	Test Conditions	Min	Type	Max	Unit
$I_{T(AV)}$	Mean On-State Current	Half Sine Wave, $T_C = 70\text{ °C}$	-	-	5250	A
$I_{T(RMS)}$	RMS On-State Current	$T_C = 70\text{ °C}$	-	-	8243	A
I_{TSM}	Surge (non-repetitive) On-State Current	10ms, Half Sine Wave, $T_C = 125\text{ °C}$, $V_R = 0$	-	-	83.5	kA
I^2t	Limiting load integral	Sine Wave, 10ms	-	-	3486	$10^4 A^2s$

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Characteristics

Symb.	Parameter	Test Conditions	Min	Type	Max	Unit
V_{TM}	Peak on-state voltage	$T_{vj} = 125\text{ }^{\circ}\text{C}$, $I_{TM} = 6000\text{ A}$	-	-	1.58	V
I_{DRM}	Forward leakage current	$T_{vj} = 125\text{ }^{\circ}\text{C}$, V_{DRM}	-	-	700	mA
I_{RRM}	Reverse leakage current	$T_{vj} = 125\text{ }^{\circ}\text{C}$, V_{RRM}	-	-	700	mA
V_{TO}	Threshold voltage	$T_{vj} = 125\text{ }^{\circ}\text{C}$	-	-	0.98	V
r_T	Slope resistance	$T_{vj} = 125\text{ }^{\circ}\text{C}$	-	-	0.1	m Ω
I_H	Holding current	$T_{vj} = 25\text{ }^{\circ}\text{C}$	-	-	200	mA
I_L	Latching current	$T_{vj} = 25\text{ }^{\circ}\text{C}$	-	-	1000	mA

Dynamic Parameters

Symb.	Parameter	Test Conditions	Min	Type	Max	Unit
dv/dt	Critical rate of rise of off-state voltage	$T_{vj} = 125\text{ }^{\circ}\text{C}$, $0.67V_{DRM}$	1000	-	-	V/ μ s
di/dt	Critical rate of rise of on-state current	$T_{vj} = 125\text{ }^{\circ}\text{C}$, $V_{DM} = 0.67V_{DRM}$, $f = 50\text{ Hz}$, $I_{TM} = 5000\text{ A}$ $I_{FG} = 2\text{ A}$, $tr = 0.5\text{ }\mu\text{s}$	-	-	200	A/ μ s
t_q	Turn-off time	$T_{vj} = 125\text{ }^{\circ}\text{C}$, $t_p = 1000\text{ }\mu\text{s}$, $V_{DM} = 0.67V_{DRM}$, $f = 1\text{ Hz}$, $dv/dt = 20\text{ V}/\mu\text{s}$, $V_R = 200\text{ V}$, $-di/dt = 1.5\text{ A}/\mu\text{s}$, $I_T = 2000\text{ A}$	-	800	-	μ s
Q_r	Recovery Charge	$T_{vj} = 125\text{ }^{\circ}\text{C}$, $-di/dt = 1.5\text{ A}/\mu\text{s}$, $t_p = 700\text{ }\mu\text{s}$, $I_T = 2000\text{ A}$, $V_R = 200\text{ V}$, trapezoid wave	-	5500	-	μ C

Gate Parameters

Symb.	Parameter	Test Conditions	Min	Type	Max	Unit
I_{GT}	Gate trigger current	$T_{vj} = 25\text{ }^{\circ}\text{C}$	-	-	400	mA
V_{GT}	Gate trigger voltage	$T_{vj} = 25\text{ }^{\circ}\text{C}$	-	-	2.6	V
I_{GD}	Gate non-trigger current	$T_{vj} = 125\text{ }^{\circ}\text{C}$, $V_D = 0.4V_{DRM}$	10	-	-	mA
V_{GD}	Gate non-trigger voltage	$T_{vj} = 125\text{ }^{\circ}\text{C}$, $V_D = 0.4V_{DRM}$	0.3	-	-	V
V_{FGM}	Peak forward gate voltage		-	-	12	V
V_{RGM}	Peak reverse gate voltage		-	-	5	V
I_{FGM}	Peak forward gate current		-	-	4	A
P_{GM}	Gate power losses		-	-	20	W
$P_{G(AV)}$	Gate power losses (mean)		-	-	4	W

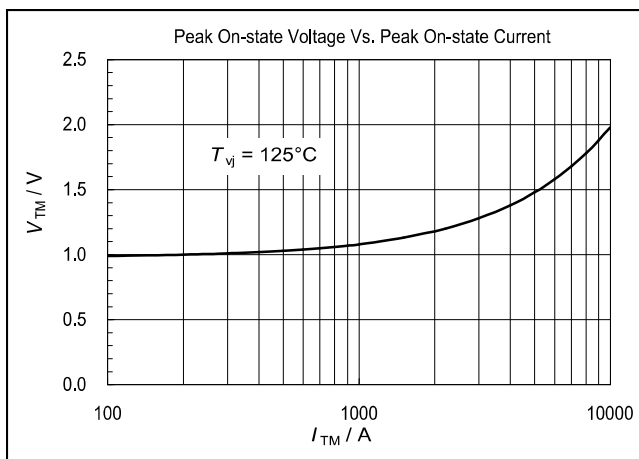


Fig1. Peak On-state Voltage Vs. Peak On-state Current

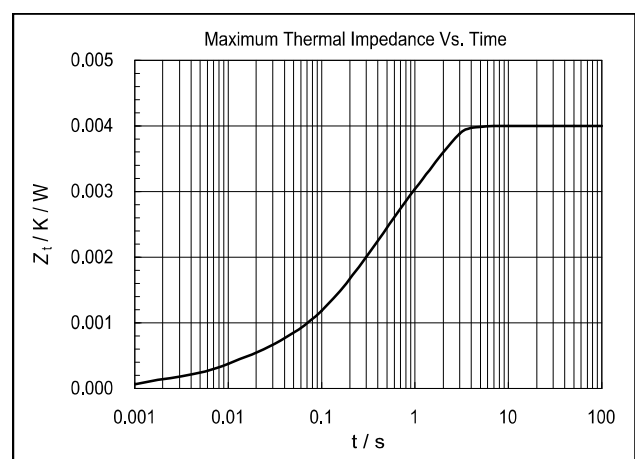


Fig2. Maximum Thermal Impedance Vs. Time

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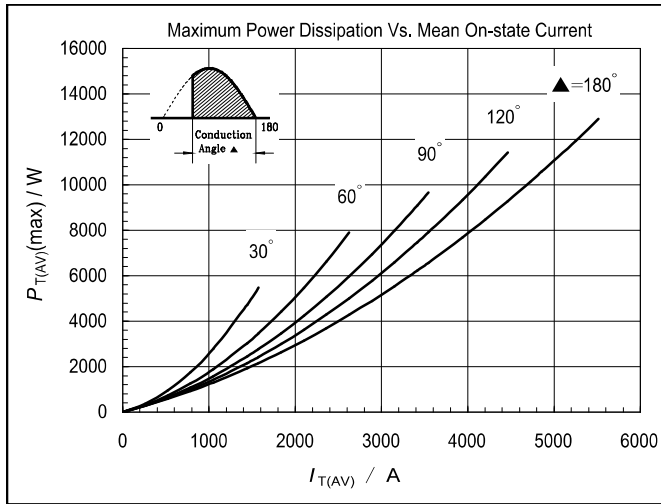


Fig3. Maximum Power Dissipation Vs. Mean On-state Current

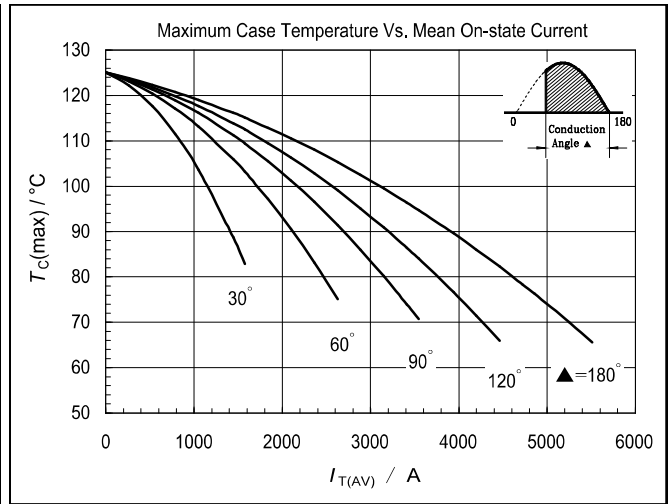


Fig4. Maximum Case Temperature Vs. Mean On-state Current

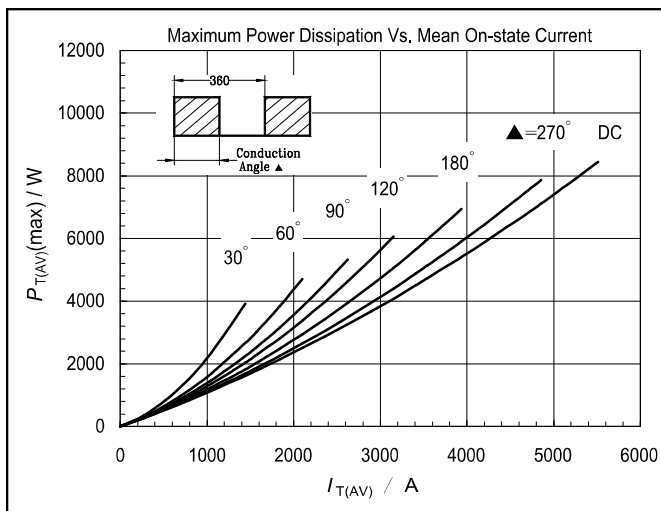


Fig5. Maximum Power Dissipation Vs. Mean On-state Current

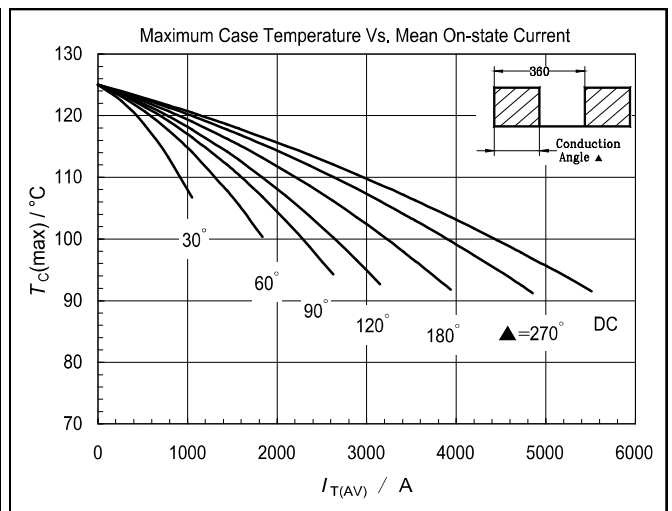


Fig6. Maximum Case Temperature Vs. Mean On-state Current

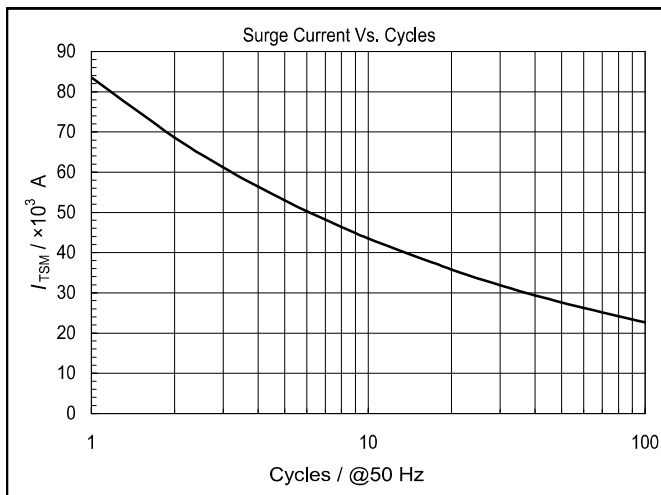


Fig7. Surge Current Vs. Cycles

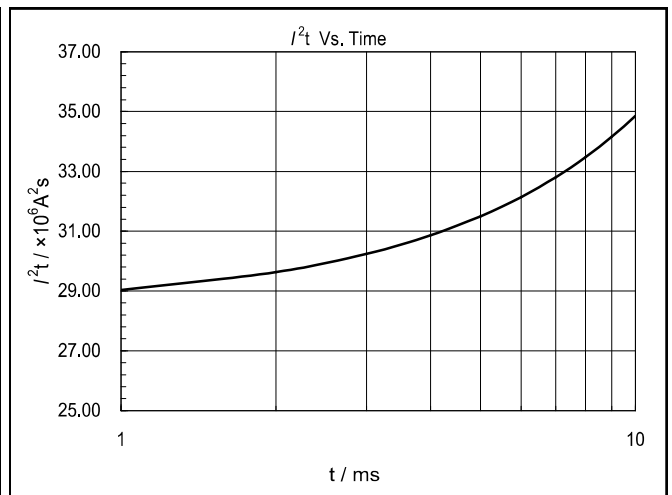


Fig8. I^2t Vs. Time

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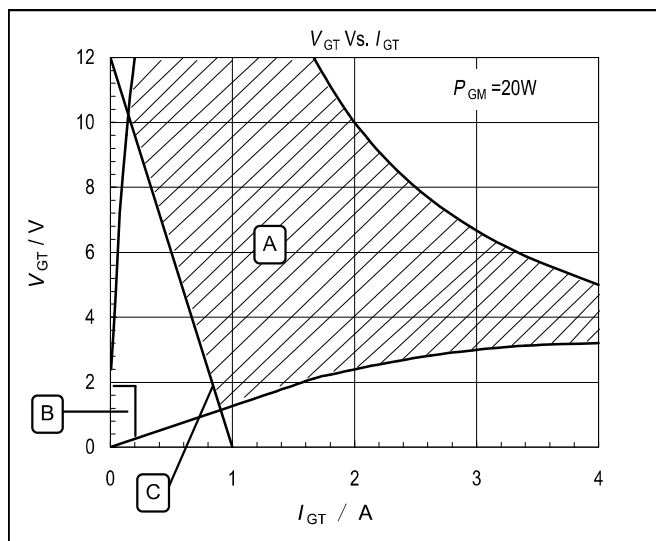


Fig9. V_{GT} Vs. I_{GT}

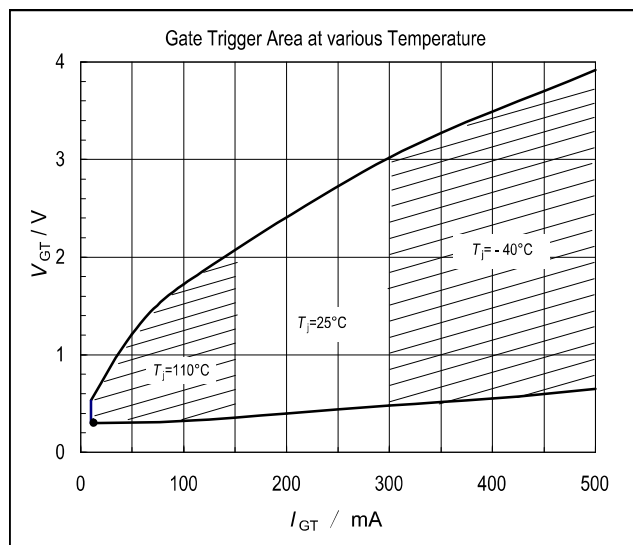


Fig10. Gate Trigger Area at various Temperature

A is Recommended Triggering Area.

B is Unreliable Triggering Area.

C is Recommended Gate Load Line.